	Application No.	Application
	Application No.	Applicant(s)
Notice of Allemaniity	10/717,986	ELORANTA ET AL
Notice of Allowability	Examiner	Art Unit
	Quochien B Vuong	2685
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>11/20/2003</u> .		
2. The allowed claim(s) is/are <u>1-5</u> .		
3. The drawings filed on 20 November 2003 are accepted by the Examiner.		
<ul> <li>4.</li></ul>		
<ul> <li>Attachment(s)</li> <li>1. ☑ Notice of References Cited (PTO-892)</li> <li>2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date 02/23/04</li> <li>4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material</li> </ul>	6. Interview Summary Paper No./Mail Dat 08), 7. Examiner's Amendr	te

Application/Control Number: 10/717,986 Page 2

Art Unit: 2685

## Reasons for Allowance

1. Claims 1-5 are allowed over the cited prior art.

2. The following is an examiner's statement of reasons for allowance:

Regarding independent claims 1 and 5, the Applicant admitted prior art (AAPA) (figure 2) disclose a digital to RF-conversion method and device for converting a digital signal having a plurality of data bits for providing a differential output signal modulated by a carrier signal, the carrier signal provided between two carrier signal ends, wherein the differential output signal is formed with current loads and provided between two output ends, the device comprising: a plurality of conversion units connected in parallel (column 1, lines 17-27), each unit adapted to receive a control voltage indicative of a data signal value, the control voltage provided between two control voltage ends, each unit comprising a differential switch pair (figure 3, page 1, lines 28-34). And Comer (US 4,088,905) discloses a circuit for digital to analog converter comprising a first and second differential switch sections (figure 1, column 3, lines 25-68). However, the AAPA and Comer fail to teach the method and device above wherein the first differential switch section having: two input current paths, each operatively connected to a different one of the output ends; and two differential switch pairs connected to the control voltage ends for conveying in the two input current paths differential currents indicative of the data signal value; the second differential switch section having two control current paths, each operatively connected in series to a different one of the two differential switch pairs, the control current paths operatively and separately connected to different ones of the carrier signal ends, for modulating the differential currents with the carrier

signal; and a current source, operatively connected in series to the second differential switch section for further controlling currents in the control current paths.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Information Disclosure Statement

3. The information disclosure statement (IDS) submitted on 02/23/2004 is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

## Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Grandfield (US 5,448,772) discloses a stacked double balanced mixer circuit.

Ylamurto et al. (US 6,545,516) disclose frequency conversion.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quochien B Vuong whose telephone number is (571) 272-7902. The examiner can normally be reached on M-F 9:30-18:00.

Application/Control Number: 10/717,986 Page 4

Art Unit: 2685

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward Urban can be reached on (571) 272-7899. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

QUOCHIEN B. YUONG PRIMARY EXAMINER

Quochien B. Vuong

Apr. 29, 2005.